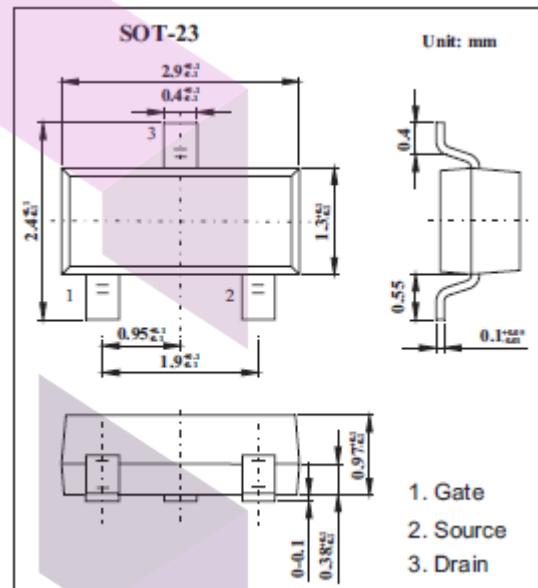
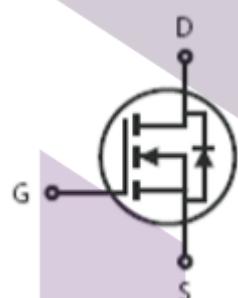


N-Channel Enhancement Mode Field Effect Transistor

SI2300

■ Features

- ◆ V_{DS}=20V,R_{DS(ON)}=40mΩ@V_{GS}=4.5V, I_D=5.0A
- ◆ V_{DS}=20V,R_{DS(ON)}=60mΩ@V_{GS}=2.5V, I_D=4.0A
- ◆ V_{DS}=20V,R_{DS(ON)}=75mΩ@V_{GS}=1.8V, I_D=1.0A



■ Absolute Maximum Ratings Ta=25°C

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±10	V
Drain-Current -Continuous* TJ=125°C -Pulsed	I _D	3.8	A
	I _{DM}	15	A
Power Dissipation*	P _D	1.25	W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	100	°C/W
Operating Junction and Storage Temperature Range	T _{j,Tstg}	-55 to 150	°C

*Surface Mounted on FR4 Board, t≤10sec.

■ Electrical Characteristics Ta=25°C

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{DSS}	V _{GS} =0V, I _D =250μA	20			V
Zero Gate Voltage Drain Current	I _{DSS}	V _D =20V, V _{GS} =0V			1	μA
Gate-Body Leakage	I _{GSS}	V _{GS} =±10V, V _D =0V			±100	nA
Gate Threshold Voltage*	V _{G(th)}	V _{GS} =V _D , I _D =250μA	0.6	0.78	1.5	V
Drain-Source On-state Resistance*	R _{D(ON)}	V _{GS} =4.5V, I _D =5.0A		32	40	mΩ
		V _{GS} =2.5V, I _D =4.0A		50	60	mΩ
		V _{GS} =1.8V, I _D =1.0A		62	75	mΩ
On-State Drain Current*	I _{D(ON)}	V _D =5V, V _{GS} =4.5V	18			A
Forward Transconductance*	g _{FS}	V _D =5V, I _D =5.0A	5			S
Input Capacitance	C _{ISS}	V _D =15V, V _{GS} =0V, f=1.0MHz		888		pF
Output Capacitance	C _{OSS}			144		pF
Reverse Transfer Capacitance	C _{RSS}			115		pF
Turn-On Delay Time	t _{D(on)}	V _D =10V, I _D =1A, V _{GS} =4.5V, RL=10 Ω, R _{GEN} =6Ω		31.8		ns
Rise Time	t _r			14.5		ns
Turn-Off Delay Time	t _{D(off)}			50.3		ns
Fall Time	t _r			31.9		ns
Total Gate Charge	Q _g	V _D =10V, I _D =3.5A, V _{GS} =4.5V		16.8		nC
Gate-Source Charge	Q _{gs}			2.5		nC
Gate-Drain Charge	Q _{gd}			5.4		nC
Drain-Source Diode Forward Current*	I _s				1.25	A
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _s =1.25A		0.825	1.2	V

*Pulse Test: Pulse Width ≤300μs, Duty Cycle≤2%

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